

## AMENDMENTS

### IN THE CLAIMS:

1. (Currently Amended) A synchronization method for a reception unit, said method comprising:

transmitting synchronization signals from a transmission unit to at least one reception unit;

supplying said synchronization signals from said at least one reception unit to a first clock transmitter, said first clock transmitter outputting a stable number of clock signals between two of said synchronization signals; and

driving a second clock transmitter using said stable clock signals, said second clock transmitter generating a second clock signal which is continuously present, even when said stable clock signals are absent, wherein a phase difference arising between said first clock transmitter and said second clock transmitter is compensated for by influencing a period duration of said second clock transmitter, wherein said period duration of said second clock transmitter is influenced such that a shorter interval between phases of said stable clock signals and said second clock signal are reduced.

2. (Previously Presented) The method of claim 1, wherein only slight changes in said period duration of said second clock transmitter are made such that said phase difference is continuously reduced within a prescribed time period until said stable clock signals and said second clock signal are synchronous with one another.

3. (Cancelled)

4. (Previously Presented) The method of claim 1 or 2, wherein said second clock transmitter is driven with a prescribed standard period duration in the event said first stable clock signals are absent.

5. (Previously Presented) The method of claims 1 or 2, wherein said at least one reception unit supplies said synchronization signals to said first clock transmitter via a phase regulator in a phase locked loop.

6. (Currently Amended) The method of claim 59, wherein corrections in said period duration of said first clock transmitter, which are ascertained by said phase regulator from clock pulse to clock pulse, are taken into account both in said stable clock signal and in said second clock signal.

7. (Currently Amended) A reception unit for synchronizing signals, said reception unit comprising:

a first clock transmitter, said first clock transmitter outputting a stable number of clock signals between two of synchronization signals received from a transmission unit; and

a second clock transmitter, said second clock transmitter using said stable clock signals, said second clock transmitter generating a second clock signal which is continuously present, even when said stable clock signals are absent, wherein a phase difference arising between said first clock transmitter and said second clock transmitter is compensated for by influencing a period duration of said second clock transmitter, wherein fluctuations in said period duration of said first clock transmitter, which are corrected by said phase regulator, are mapped onto said second clock transmitter.

8. (Currently Amended) The method of claim 57, wherein said phase regulator, upon receiving said synchronization signals, ascertains instantaneous phase errors and readjusts said first clock transmitter such that said first clock transmitter outputs a nominal number of clock signals between two synchronization signals.

9. (Cancelled)